

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1286	"361"/\$.ccls. and motherboard and (slots or connector) and memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/09 18:23
L2	225	"361"/\$.ccls. and motherboard and (slots or connector) and memory and resistors	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/09 17:58
L3	140	"361"/\$.ccls. and motherboard and (slots or connector) and memory and resistors and capacitor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/09 17:58
L4	15	("5283877"   "5319591"   "5341331"   "5357624"   "5383148"   "5513135"   "5564107"   "5646904"   "5661677"   "5691950"   "5761132"   "5802395"   "5870325"   "5926838"   "5945886").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/12/09 18:03
L5	11	("4342415"   "4731699"   "4739448"   "4761881"   "4922324"   "4970624"   "5102712"   "5128506"   "5176349"   "5223691"   "5270903").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/12/09 18:09
L6	9	("5488539").URPN.	USPAT	OR	ON	2005/12/09 18:09
L7	458	"361"/\$.ccls. and motherboard and ((slots or connector) with memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/09 18:12
L8	470	(termination adj circuit) with resistors	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/09 18:24
L9	4	7 and 8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/09 18:11

L10	0	(termination adj circuit) with resistors with (memory adj slots)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/09 18:12
L11	107	"361"/\$.ccls. and motherboard and ((slots or connector) with memory) and resistors	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/09 18:40
L12	5	("5805903"   "5884053"   "6215686"   "6362996"   "6449679").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/12/09 18:17
L13	3	("5706445"   "5867417"   "5877975").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/12/09 18:18
L14	16	("3191095"   "3992686"   "4401358"   "4418971"   "4420793"   "4468612"   "4575780"   "4658333"   "4675769"   "4984064"   "5103247"   "5105340"   "5117331"   "5162979"   "5203004").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/12/09 18:21
L15	14	("5117331").URPN.	USPAT	OR	ON	2005/12/09 18:22
L16	511	"361"/\$.ccls. and (motherboard with slots)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/09 18:24
L17	6	(termination adj circuit adj module)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/09 18:26
L18	1691	(termination near3 module)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/09 18:26
L19	6	(termination near3 module) with resistors with capacitors	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/09 18:29
L20	9	(module with resistors with capacitors) with slots	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/09 18:32

L21	196	361/803.ccls. and slot	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/09 18:32
L22	9	("5440755"   "5604871"   "5754796"   "5852725"   "6044411"   "6147863"   "6289405"   "6349037"   "6400843").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/12/09 18:33
L23	35	("5440755").URPN.	USPAT	OR	ON	2005/12/09 18:36
L24	146	"710"/\$.ccls. and motherboard and ((slots or connector) with memory) and resistors	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/09 18:46
L25	11	("4660141"   "5096090"   "5239559"   "5367697"   "5467455"   "5473264"   "5495584"   "5585741"   "5629831"   "5706447"   "5748910").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/12/09 18:43
L26	46	365/52-63.ccls. and motherboard and ((slots or connector) with memory) and resistors	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/09 18:46